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Commissioner for Patents  
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*Attn: Mail Stop Post Issue*

Re: U.S. Patent No. 7,163,856; Issued: January 16, 2007  
(*U.S. Patent Application No. 10/714,271; Filing Date: November 13, 2003*)  
For: **Method of Fabricating a Lateral Double-Diffused MOSFET  
(LDMOS) Transistor and a Conventional CMOS Transistor**  
Inventors: ZUNIGA *et al.*  
Our Ref: 2912.011REXO

Sir:

Transmitted herewith for appropriate action is a Change of Correspondence Address - *Patent*. This document is filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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JEW/CAC/vb  
Enclosure

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